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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/666809-Conf. #8752
Filing Date	September 17, 2003
First Named Inventor	Pieter J. MOSTERMAN
Art Unit	2125
Examiner Name	A. W. Paladini

Sheet	1	of	1	Attorney Docket Number	MWS-024

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.[1]	Document Number — Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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AP	C1	Guasch, A. et al, "Precompiled Submodels: A General Sorting Procedure," *Proceedings of the 2nd European Simulation Congress*, Sept. 9-12, 1986, pp. 172-178	
	C2	Thomas, Drea, "Q&A," *The MathWorks MATLAB Digest*, Vol. 3(1):1-8 (1995)	
	C3	Mosterman, Pieter J. et al, "Using Interleaved Execution to Resolve Cyclic Dependencies in Time-Based Block Diagrams," *43rd IEEE Conference on Decision and Control*, Dec. 14-17, 2004, pp. 4057-4062	
	C4	Pearce, J.G., "The Submodel Concept in Continuous System Simulation Languages," *Simulation of Dynamical Systems, IEE Colloquium on Simulation of Dynamical Systems*, pp. 2/1-2/3 (2002)	
	C5	"Using Simulink Version 5," The MathWorks, Inc., (2002)	
	C6	"Using Simulink Version 6," The MathWorks, Inc. (2004)	
	C7	International Search Report for Application No. PCT/US2004/030098, dated June 13, 2006	

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